

Amendments to the Specification

Please replace the paragraph on page 1, lines 10-13 with the following paragraph:

1. United States patent application entitled, “~~Automatic generation of hardware description language code for COMplex polynomial functions~~” “Automatic Generation of Hardware Description Language Code for Complex Polynomial Functions”, attorney docket no.: M-8319 US Serial No. 09/822,713, naming Andrew J. Thurston and Douglas Duschatko as inventors and filed ~~substantially contemporaneously with the present application~~ March 30, 2001;

Please replace the paragraph on page 1, lines 14-17 with the following paragraph:

2. United States patent application entitled, “BCH Forward Error Correction Decoder”, attorney docket no.: M-8342 US Serial No. 09/822,950, naming Andrew J. Thurston as inventor and filed ~~substantially contemporaneously with the present application~~ March 30, 2001; and

Please replace the paragraph on page 1, lines 18-20 with the following paragraph:

3. United States patent application entitled, “Galois Field Multiply Accumulator”, attorney docket no.: M-8341 US Serial No. 09/822,733, naming Andrew J. Thurston as inventor and filed ~~substantially contemporaneously with the present application~~ March 30, 2001.